

Alder Lake S BIOS size list

BIOS Version Trunk rev.366	Flash ROM	NVS (NvStorage+FtwWorking+ FtwSpare+FactoryCo		BVDT		DMI		MSDM		Variable default	
		Total	Free	Total	Free	Total	Free	Total	Free	Total	Free
Release	16384	512	N/A	4	0	4	0	64	0	192	0
EFI_Debug	16384	512	N/A	4	0	4	0	64	0	192	0

(size: K)

Note:

1. This is Alder Lake S code size estimation based on Trunk 653
2. N/A: Not Available for project use
3. The EFI_DEBUG build needs put the source code to root, and change the name shorter. EFI_Debug command: nmake efidebug
5. TXT, Boot Guard and BIOS Guard features enabled.

Alder Lake P BIOS size list

BIOS Version Trunk rev.366	Flash ROM	NVS (NvStorage+FtwWorking+ FtwSpare+FactoryCo		BVDT		DMI		MSDM		Variable default	
		Total	Free	Total	Free	Total	Free	Total	Free	Total	Free
Release	16384	512	N/A	4	0	4	0	64	0	136	0
EFI_Debug	16384	512	N/A	4	0	4	0	64	0	136	0

(size: K)

Note:

1. This is Alder Lake P code size estimation based on Trunk 653
2. N/A: Not Available for project use
3. The EFI_DEBUG build needs put the source code to root, and change the name shorter. EFI_Debug command: nmake efidebug
5. TXT, Boot Guard and BIOS Guard features enabled.

DXE + Logo		FW Resiliency		Binary FV		Microcode		Recovery2		Recovery		FSF
Total	Free	Total	Free	Total	Free	Total	Free	Total	Free	Total	Free	Total
5336	2620	4096	0	512	112	512	173.9	244	116.39	536	319.42	700
5336	2072	0	0	512	112	512	173.9	2090	1740	2660	1960	700

BootGuardAcm 144K
 BiosGuard Acm 34K
 TXT BIOS Acm 120K

DXE + Logo		FW Resiliency		Binary FV		Microcode		Recovery2		Recovery		FSF
Total	Free	Total	Free	Total	Free	Total	Free	Total	Free	Total	Free	Total
5336	2616	4096	0	512	112	512	321.9	244	108.39	536	205.2	580
5336	2060	0	0	512	112	512	321.9	2090	1720	2660	1940	580

BootGuard Acm 144K
 BiosGuard Acm 34K

TXT BIOS Acm 120K

P-S	FSP-M		FSP-T		Recovery0		Reserved	
	Free	Total	Free	Total	Free	Total	Free	Total
	0	1250	0	64	0	180	106.57	1896
	0	1250	0	64	0	180	28.53	1896

P-S	FSP-M		FSP-T		Recovery0		Reserved	
	Free	Total	Free	Total	Free	Total	Free	Total
	0	1120	0	56	0	180	106.16	1896
	0	1120	0	56	0	180	28	1896